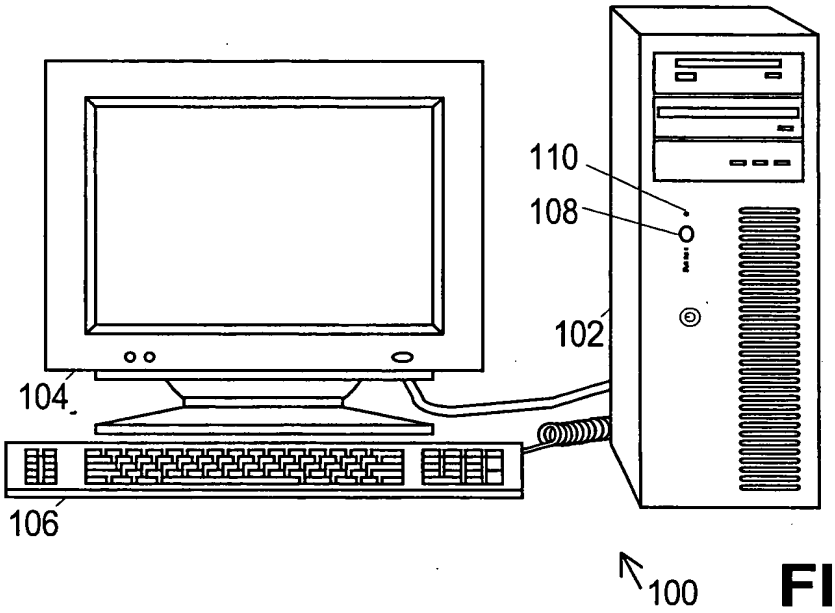
110
108
102
104
106
100

FIG. 1

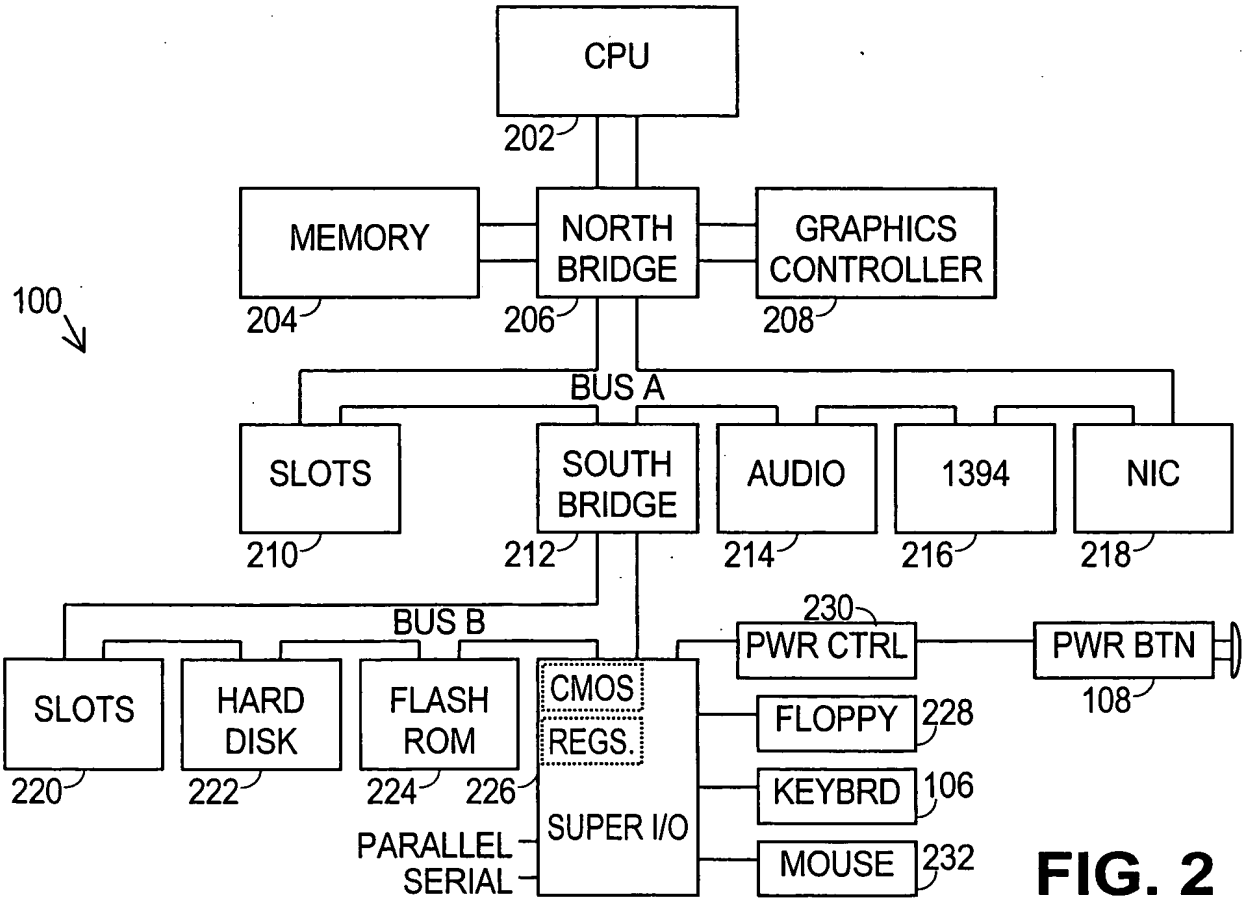
CPU
202
MEMORY
204
NORTH BRIDGE
206
GRAPHICS CONTROLLER
208
100
BUS A
SLOTS
210
SOUTH BRIDGE
212
AUDIO
214
1394
216
NIC
218
230
BUS B
PWR CTRL
PWR BTN
108
SLOTS
220
HARD DISK
222
FLASH ROM
224
226
CMOS
REGS.
SUPER I/O
PARALLEL
SERIAL
FLOPPY
228
KEYBRD
106
MOUSE
232

FIG. 2

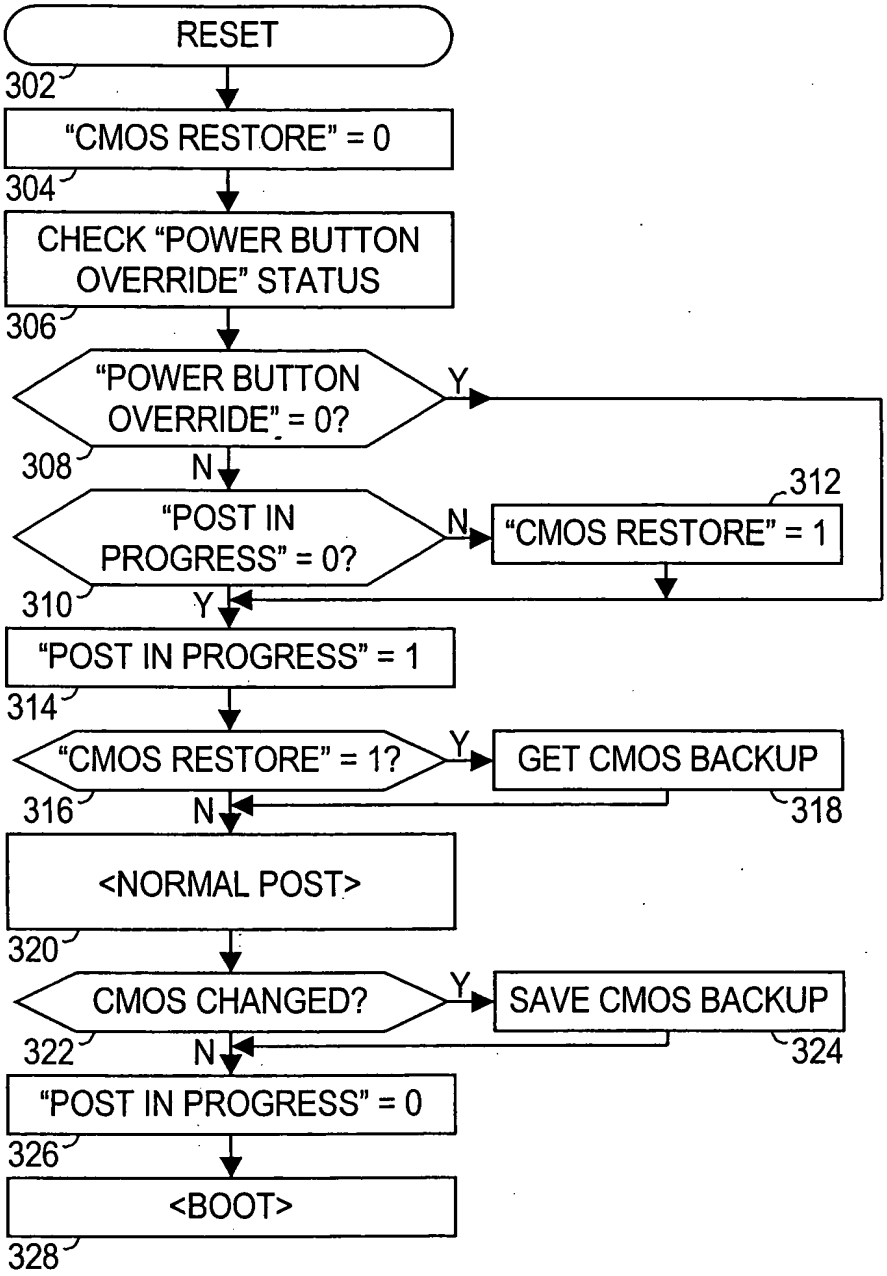
RESET
302
"CMOS RESTORE" = 0
304
CHECK "POWER BUTTON OVERRIDE" STATUS
306
"POWER BUTTON OVERRIDE" = 0?
Y
308
N
312
"POST IN PROGRESS" = 0?
N
"CMOS RESTORE" = 1
310
Y
"POST IN PROGRESS" = 1
314
"CMOS RESTORE" = 1?
Y
GET CMOS BACKUP
316
N
318
<NORMAL POST>
320
CMOS CHANGED?
Y
SAVE CMOS BACKUP
322
N
324
"POST IN PROGRESS" = 0
326
<BOOT>
328

FIG. 3

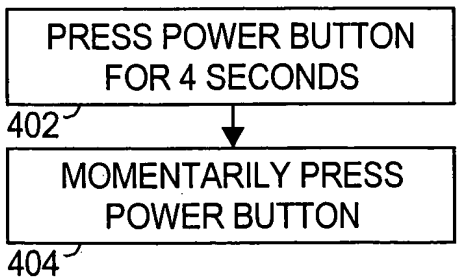
PRESS POWER BUTTON FOR 4 SECONDS
402
MOMENTARILY PRESS POWER BUTTON
404

FIG. 4